

Announcement of USTC-MEAD Series IC Courses

MEAD Education (www.mead.ch) is offering advanced engineering courses in the field of analog and mixed-signal IC design targeting the audience of electrical engineers, company managers and marketing engineers working in the semiconductor industry. The lecturers are leading practitioners and top experts in the area from high-technology companies and universities, who teach the most up-to-date information available at the time of the course. The University of Science and Technology of China (USTC) is under the jurisdiction of the Chinese Academy of Sciences (CAS). As a key university of the nation, it is noted for its advanced science and high technology programs, unique management studies and well-tailored disciplines in the humanities. USTC and MEAS are agreed to offer series advanced IC design courses in USTC campus for audients from both academics and industries. The first advanced analog IC design course will be offered in coming November.

Organizer: University of Science and Technology of China

Topic: “Low-Power & Low-Voltage Analog IC Design”

Date: November 15-17, 2012

Course Calendar :

Thursday, November 15, 2012		
8:30-12:00 am	Basic Low-Voltage, Low-Power Circuit Techniques	W.Sansen
1:30-3:00 pm	Stability of Operational Amplifiers	W.Sansen
3:30-5:00 pm	Systematic Design of Low-Power Operational Amplifiers	W.Sansen
Friday, November 16, 2012		
8:30-10:00 am	Important OpAmp Configurations	W.Sansen
10:30-12:00am	Noise Performance of Elementary Transistor Stages	W.Sansen
1:30-3:00 pm	Fully-Differential Operational Amplifiers	W.Sansen
3:30-5:00 pm	Bandgap and Current Reference Circuits	W.Sansen
Saturday, November 17, 2012		
8:30-10:00 am	Distortion in Elementary Transistor Circuits	W.Sansen
10:30-12:00am	Low-Power Continuous-Time Filters	W.Sansen

1:30-5:00 pm

Delta-Sigma Data Converters

W.Sansen

Lecturer - Willy Sansen



Prof. Willy Sansen has an MSc Degree from the K.U.Leuven and a PhD degree from the University of California, Berkeley in 1972. Since 1980 he has been full professor at the Catholic University of Leuven, in Belgium, where he has headed the ESAT-MICAS laboratory on analog design since 1984. He has been supervisor of sixty-three PhD theses and has authored and coauthored more than 635 publications and sixteen books, among which “Analog Design Essentials” (Springer 2008). He is a Fellow of the IEEE. He was program chair of the ISSCC-2002 conference and is now Past-President of the IEEE Solid-State Circuits.

(<http://www.esat.kuleuven.be/micas/index.php/people-mainmenu-26/13-staff/12-willy-sansen>)

Fee:

RMB3200/person, including notes, lunch, tea break, certificate and photos etc.